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## APPLICANTS

Yasumasa Tsukamoto, Hyogo, JAPAN;

Koji Nii, Hyogo, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

None

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-095050 03/31/2003

yes *P*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	JAPAN	28	17	1

## ADDRESS

MCDERMOTT, WILL &amp; EMERY

600 13th Street, N.W.

Washington, DC

20005-3096

## TITLE

Semiconductor memory device with back gate potential control circuit for transistor in memory cell

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